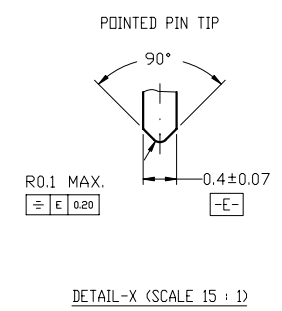
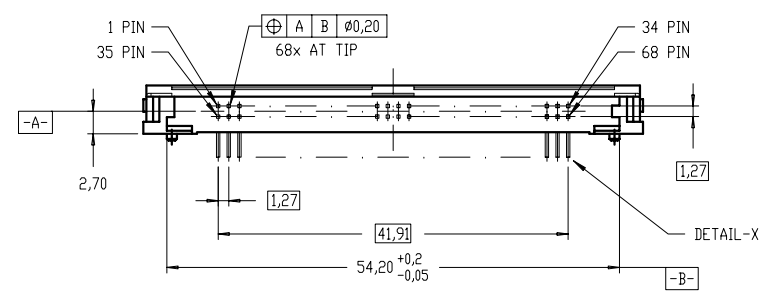
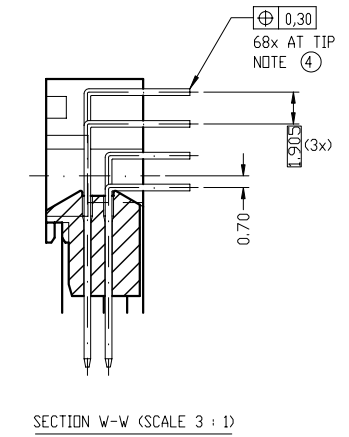
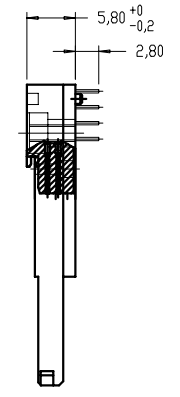
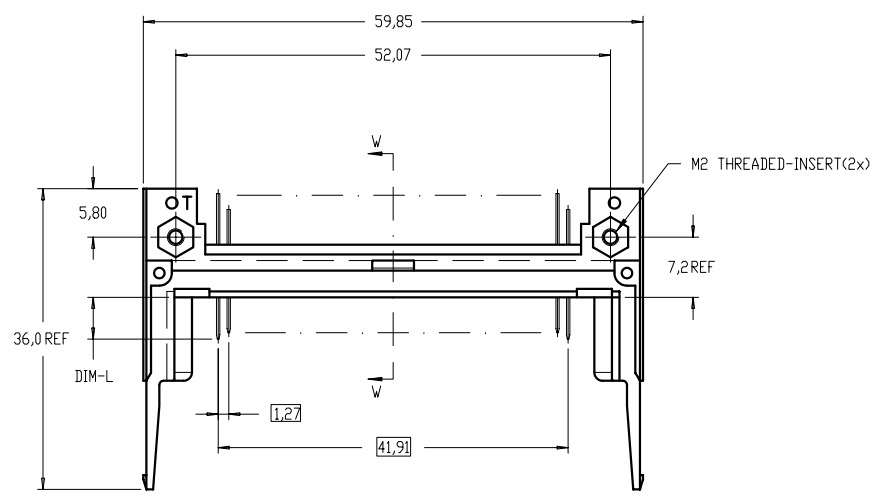


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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-000/000LF	TOP-MOUNT	BLACK	THRU-HOLE, R/A	NO	NO	NO



NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA : 2.5um MIN. Sn-Pb
- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- IF LEAD FREE P/N THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

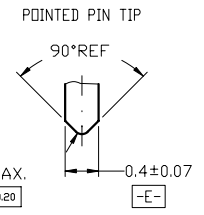
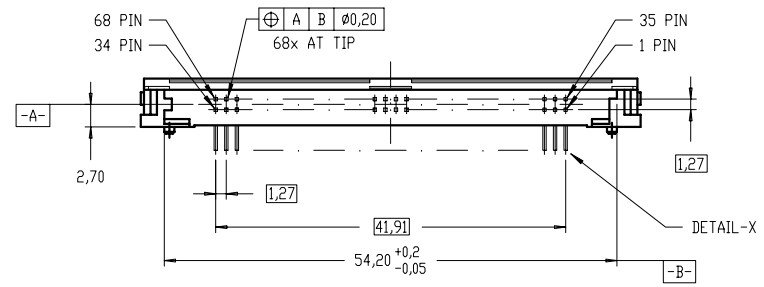
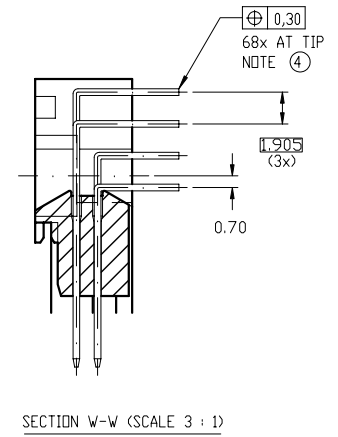
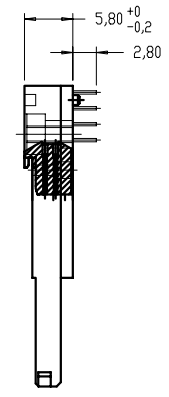
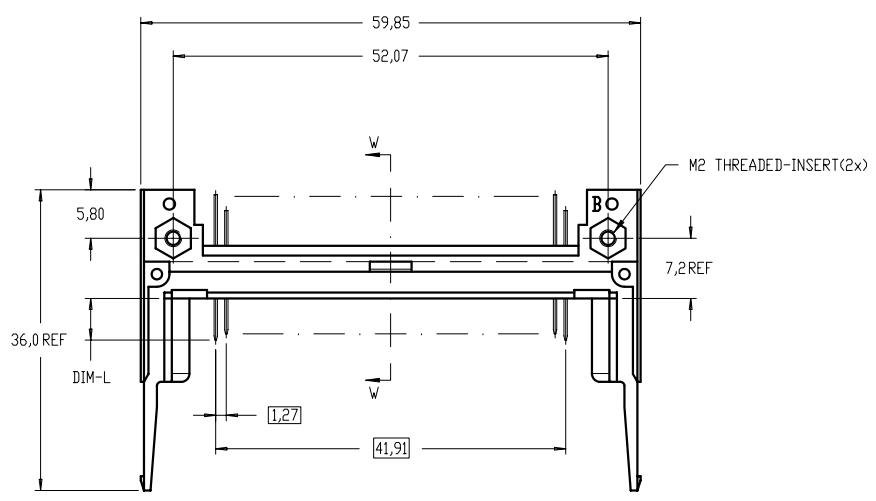
mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		 www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3	projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY		
E				angles	0° ±2°	 MM	product family	PCMCIA	code
				dr	WB JIANG 11/26/04	scale	size	dwg no	NT
				enrg	WB JIANG 11/26/04	N.T.S.	A	92856	sheet 2 of
				chr	WB JIANG 11/26/04				
				appd	JASON ZHOU 11/26/04				
sheet index	revision sheet								

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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPRT	BOARD LOCATOR
92856-500/500LF	BOTTOM-MOUNT	NATURAL	THRU-HOLE, R/A	NO	NO	NO



NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

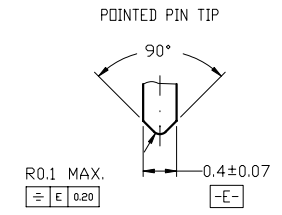
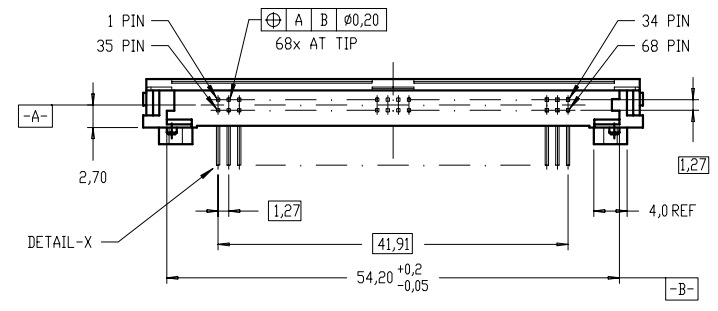
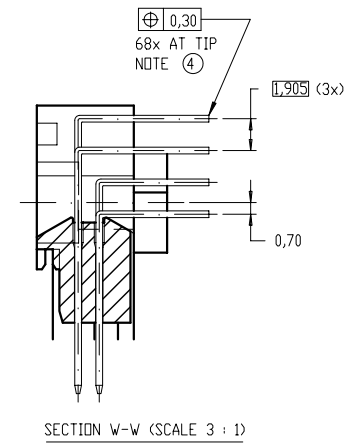
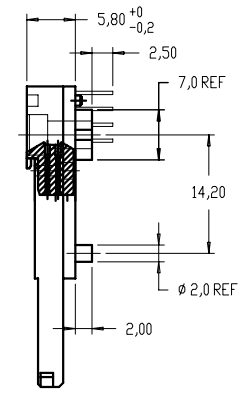
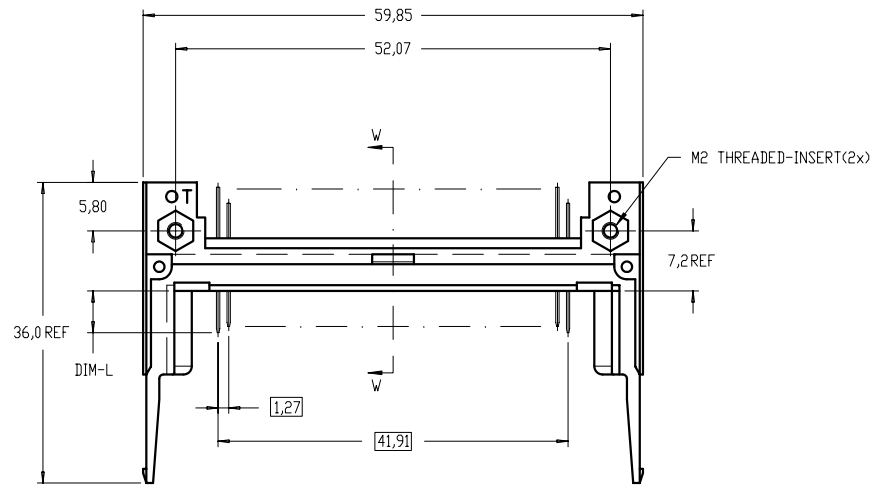
mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		 www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3	projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY		
E				angles	0° ±2°	 MM	product family	PCMCIA	code
				dr	WB JIANG 11/26/04	scale	size	dwg no	NT
				enrg	WB JIANG 11/26/04	N.T.S.	A	92856	sheet 3 of
				chr	WB JIANG 11/26/04				
				appd	JASON ZHOU 11/26/04				
sheet index	revision sheet								

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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPRT	BOARD LOCATOR
92856-040/040LF	TOP-MOUNT	BLACK	THRU-HOLE, R/A	2 mm	2 mm	NO



DETAIL-X (SCALE 15 : 1)

NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

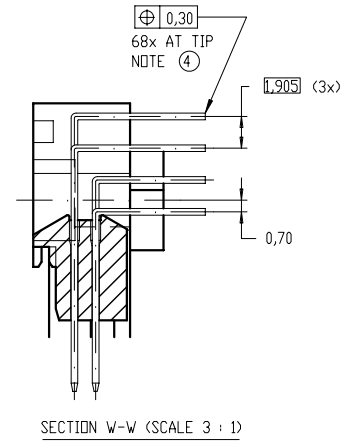
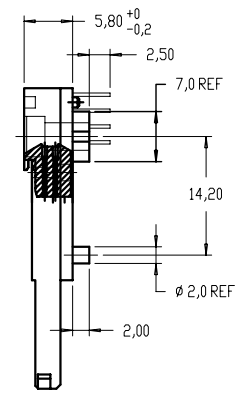
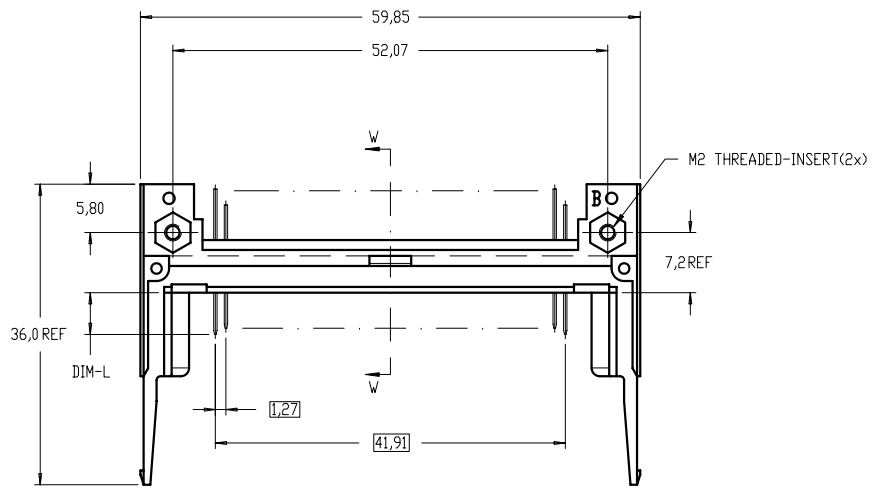
mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		 www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3	projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY		
E				angles	0° ±2°	 MM	product family	PCMCIA	code
				dr	WB JIANG 11/26/04	scale	size	dwg no	NT
				enrg	WB JIANG 11/26/04	N.T.S.	A	92856	sheet
				chr	WB JIANG 11/26/04				4 of
				appd	JASON ZHOU 11/26/04				
sheet index	revision sheet								

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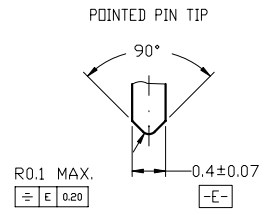
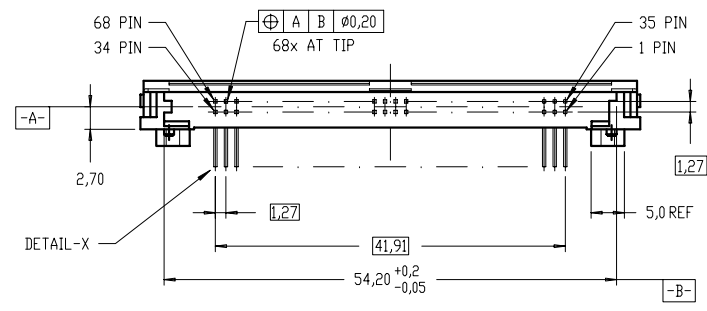
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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPRT	BOARD LOCATOR
92856-540/540LF	BOTTOM-MOUNT	NATURAL	THRU-HOLE, R/A	2 mm	2 mm	NO



NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. NI
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION



PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

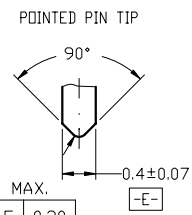
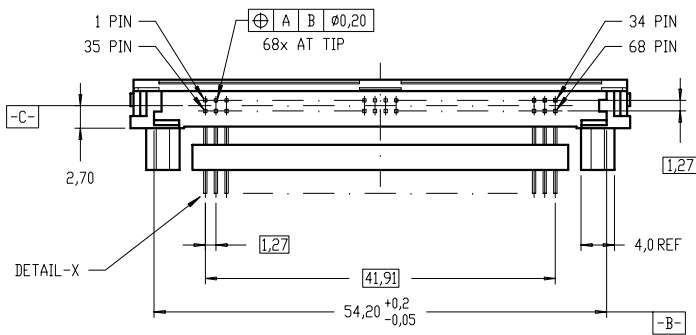
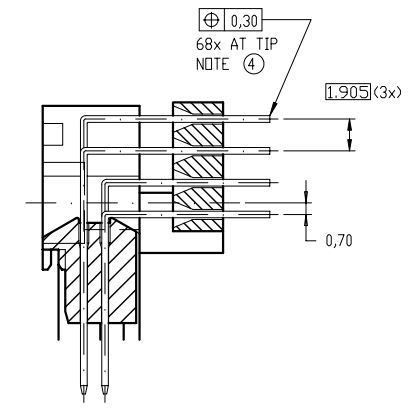
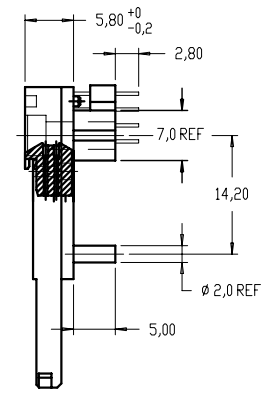
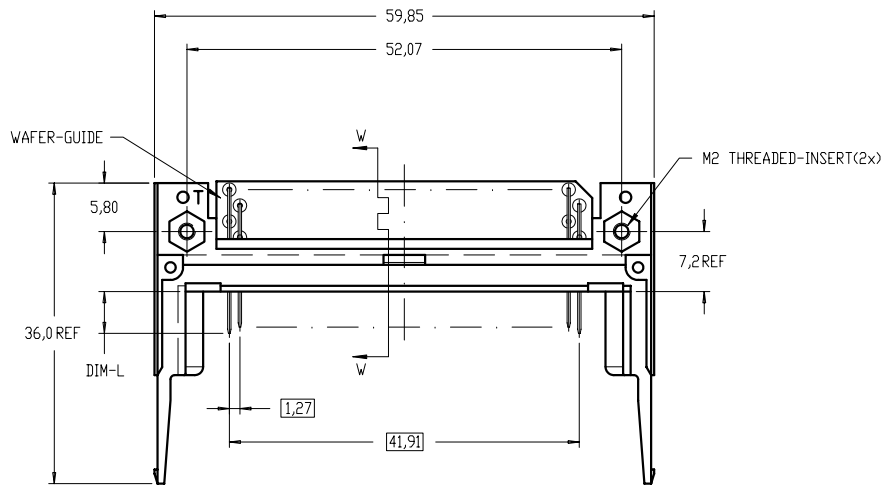
mat'l. code		tolerances unless otherwise specified		CUSTOMER COPY	 www.fciconnect.com
ltr	ecn no	dr	date	linear	
E				angles	 MM
		dr	WB JIANG 11/26/04	en	title
		en	WB JIANG 11/26/04	chr	68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY
		chr	WB JIANG 11/26/04	appd	product family
		appd	JASON ZHOU 11/26/04	scale	PCMCIA
				N.T.S.	code
sheet index	revision sheet				size
					dwg no
					92856
					sheet
					5 of

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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-020/020LF	TOP-MOUNT	BLACK	THRU-HOLE, R/A	5 mm	5 mm	NO



- NOTES :
- SEE TA-946 FOR PCB LAYOUT.
 - MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
 - FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION) : 2.5um MIN. Sn-Pb
SOLDER AREA(LEAD FREE OPTION) : 2.5um MIN. PURE TIN
 - IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
 - LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
 - IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

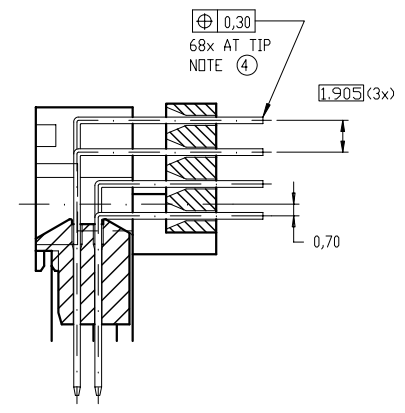
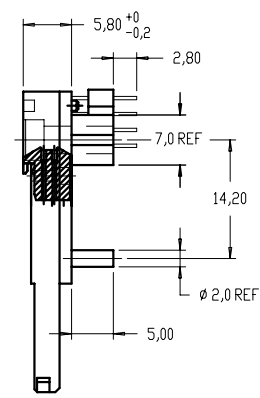
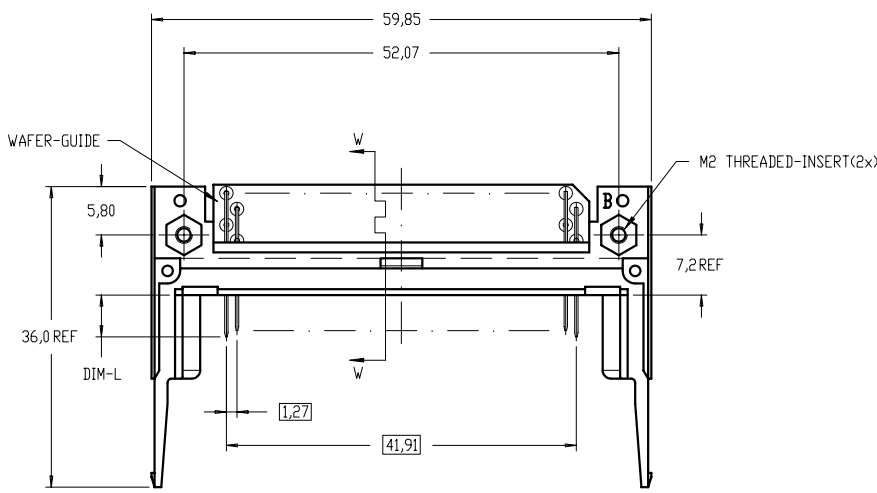
mat'l. code		tolerances unless otherwise specified		CUSTOMER COPY	 www.fciconnect.com
ltr	ecn no	dr	date	linear	
E				angles	
				dr	MM
				enrg	←
				chr	→
				appd	scale
					N.T.S.
sheet index	revision sheet			title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY product family PCMCIA code NT size dwg no 92856 sheet 6 of	

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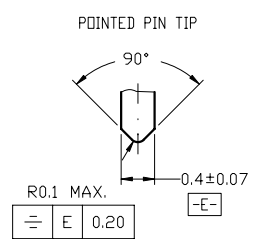
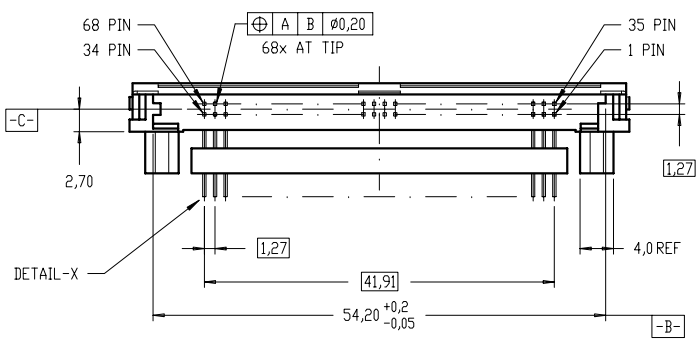
PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-520/520LF	BOTTOM-MOUNT	NATURAL	THRU-HOLE, R/A	5 mm	5 mm	NO



SECTION A-A (SCALE 3 : 1)

NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION



DETAIL-X (SCALE 15 : 1)

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

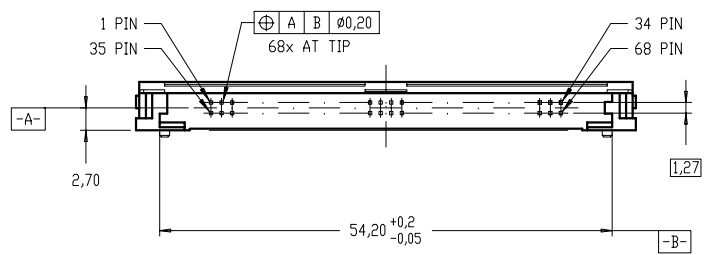
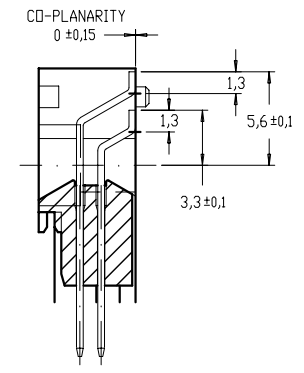
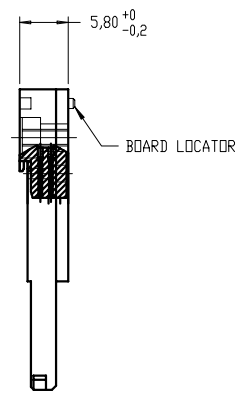
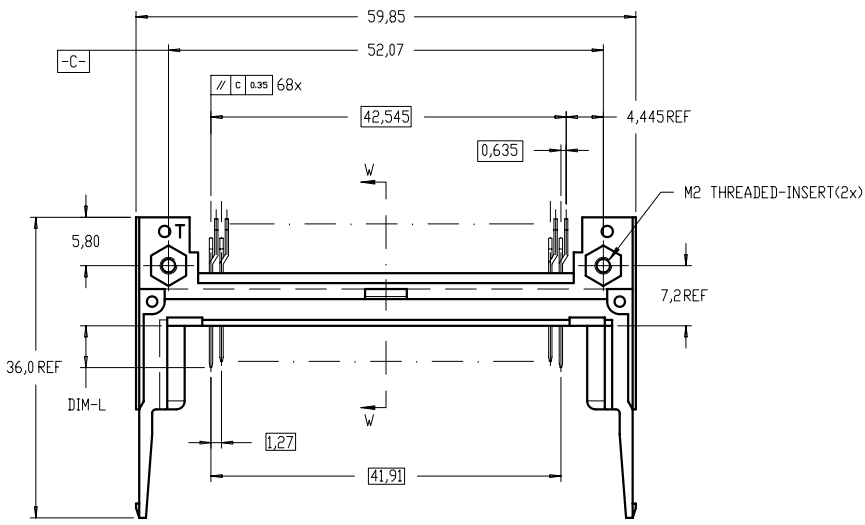
mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		 www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3	projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY		
E				angles	0° ±2°	 MM	product family	PCMCIA	code
				dr	WB JIANG 11/26/04	scale	size	dwg no	NT
				enrg	WB JIANG 11/26/04	N.T.S.	A	92856	sheet
				chr	WB JIANG 11/26/04				7 of
				appd	JASON ZHOU 11/26/04				
sheet index	revision sheet								

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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-001/001LF	TOP-MOUNT	BLACK	SMT, STAGGERED (DOUBLE-ROW)	NO	NO	YES



NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 40 SECONDS IN A CONVECTION, INFRA-RED OR VAPOR PHASE REFLOW OVEN.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		 www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3		projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY	
E					angles	0° ±2'		product family PCMCIA code NT	
				dr	WB JIANG	11/26/04	 MM	size dwg no	
				enrg	WB JIANG	11/26/04		92856	
				chr	WB JIANG	11/26/04		sheet 8 of	
				appd	JASON ZHOU	11/26/04		scale N.T.S. A	
sheet index	revision sheet								

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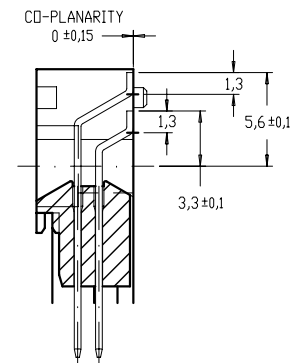
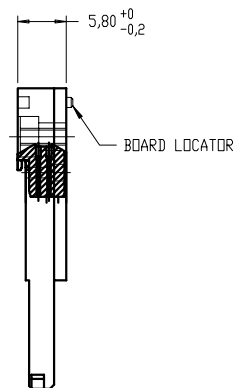
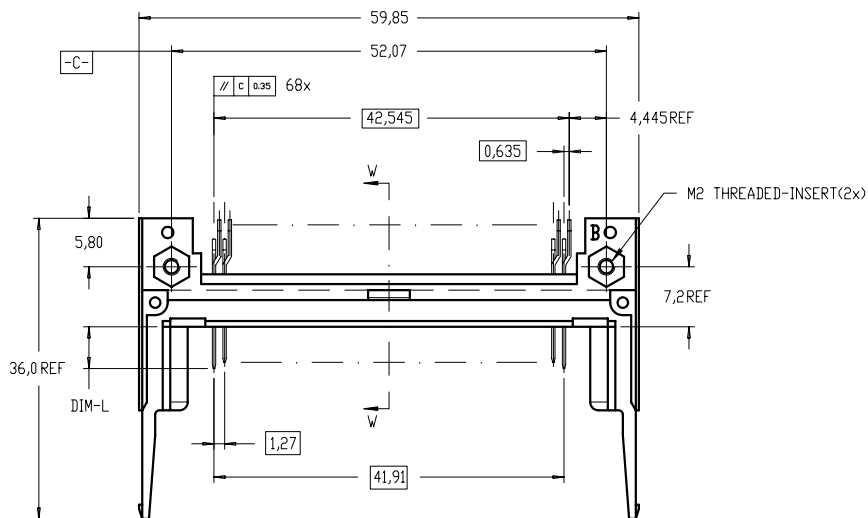
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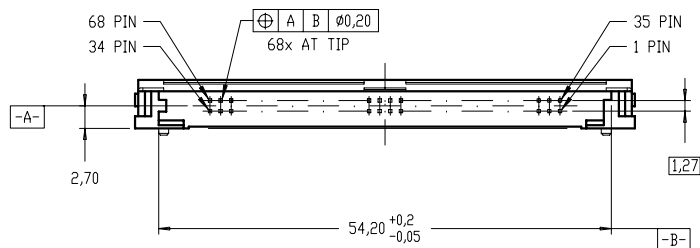
PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-501/501LF	BOTTOM-MOUNT	NATURAL	SMT, STAGGERED (DOUBLE-ROW)	NO	NO	YES



SECTION W-W (SCALE 3 : 1)

NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 40 SECONDS IN A CONVECTION, INFRA-RED OR VAPOR PHASE REFLOW OVEN.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION



PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		 www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3		projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY	
E					angles	0° ±2'		product family PCMCIA code NT	
				dr	WB JIANG	11/26/04	 MM	size dwg no	
				enrg	WB JIANG	11/26/04		92856	
				chr	WB JIANG	11/26/04		sheet 09 of	
				appd	JASON ZHOU	11/26/04		scale N.T.S. A	
sheet index	revision sheet								

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ACAD

PDM: Rev:E

STATUS: Released
cage code 2526
Printed: Sep 28, 2007

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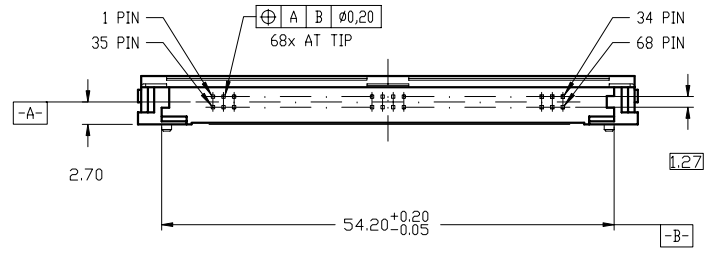
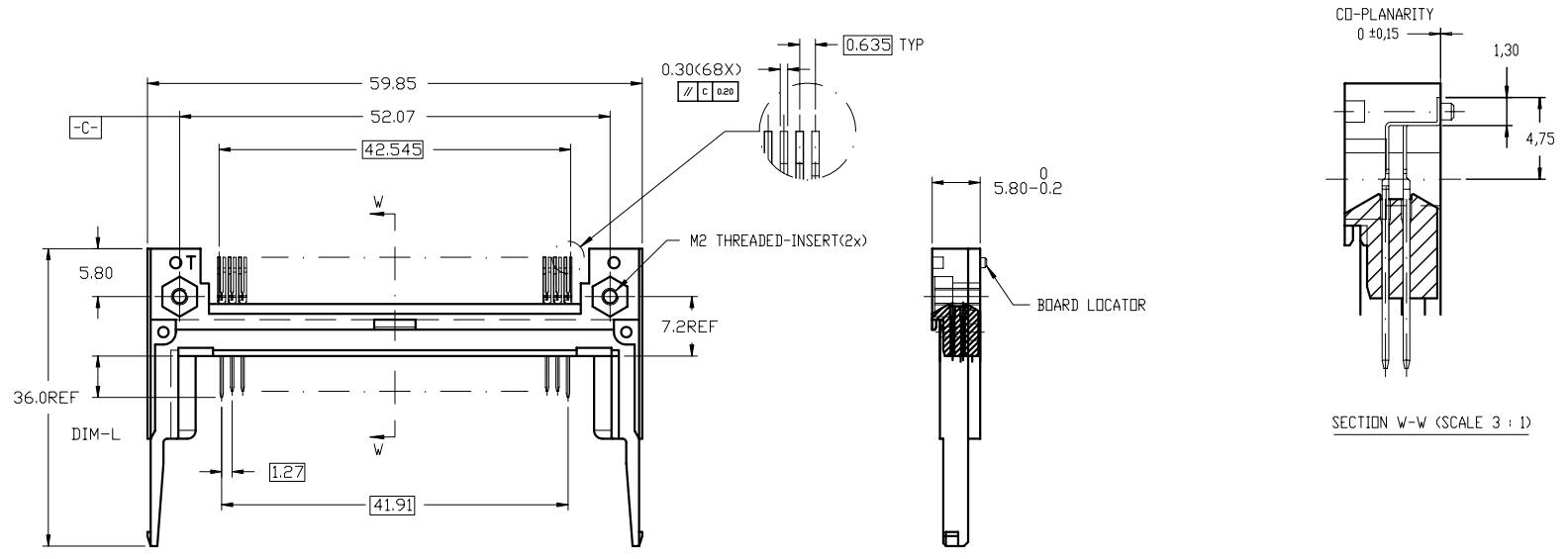
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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-002/002LF	TOP-MOUNT	BLACK	SMT IN-LINE (SINGLE-ROW)	NO	NO	YES



- NOTES :
- SEE TA-946 FOR PCB LAYOUT.
 - MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
 - FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA(LEAD FREE OPTION) : 2.5um MIN. PURE TIN
 - IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 40 SECONDS IN A CONVECTION, INFRA-RED OR VAPOR PHASE REFLOW OVEN.
 - LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
 - IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

mat'l. code		tolerances unless otherwise specified		CUSTOMER COPY	 www.fciconnect.com
ltr	ecn no	dr	date	linear	
E				angles	
				dr	MM
				enr	←
				chr	→
				appd	scale
					N.T.S.
sheet index	revision sheet			title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY product family PCMCIA code NT size dwg no 92856 sheet 10 of	

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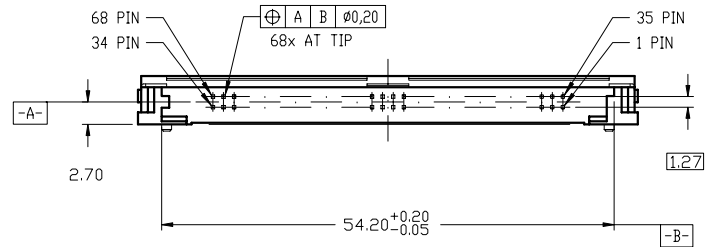
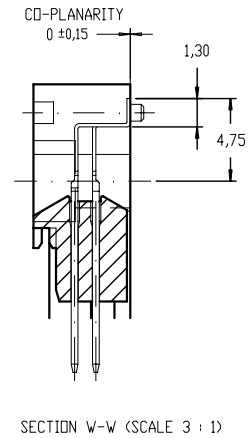
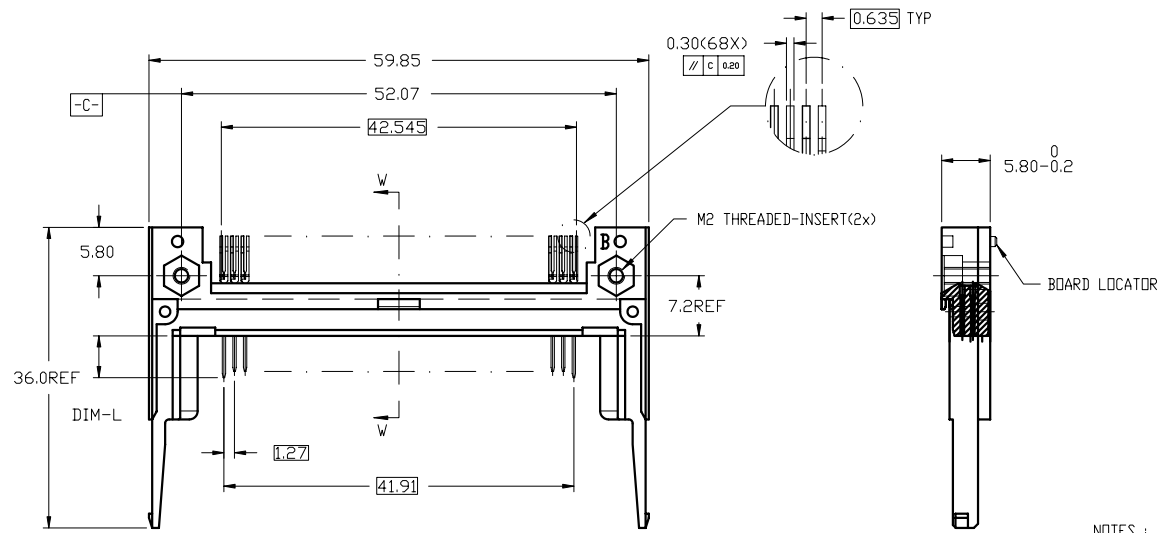
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A

B

PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-502/502LF	BOTTOM-MOUNT	NATURAL	SMT IN-LINE (SINGLE-ROW)	NO	NO	YES



NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 µm MIN. Ni
CONTACT AREA : 0.1 µm Au OVER 0.5 µm MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION) : 2.5µm MIN. Sn-Pb
SOLDER AREA(LEAD FREE OPTION) : 2.5µm MIN. PURE TIN
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 40 SECONDS IN A CONVECTION, INFRA-RED OR VAPOR PHASE REFLOW OVEN.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

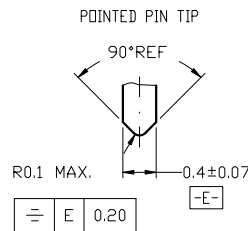
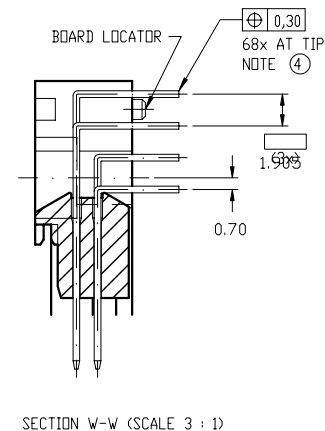
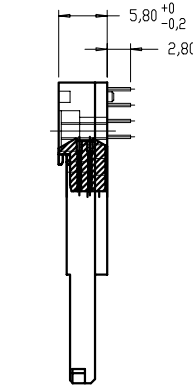
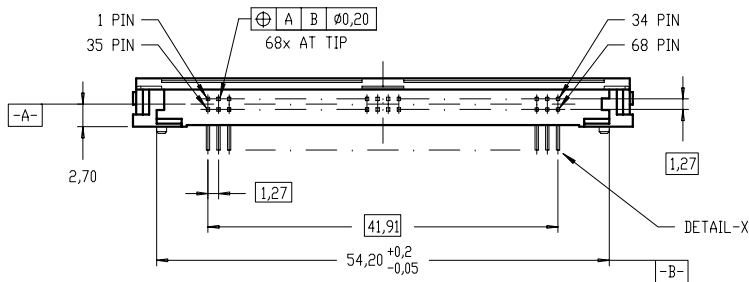
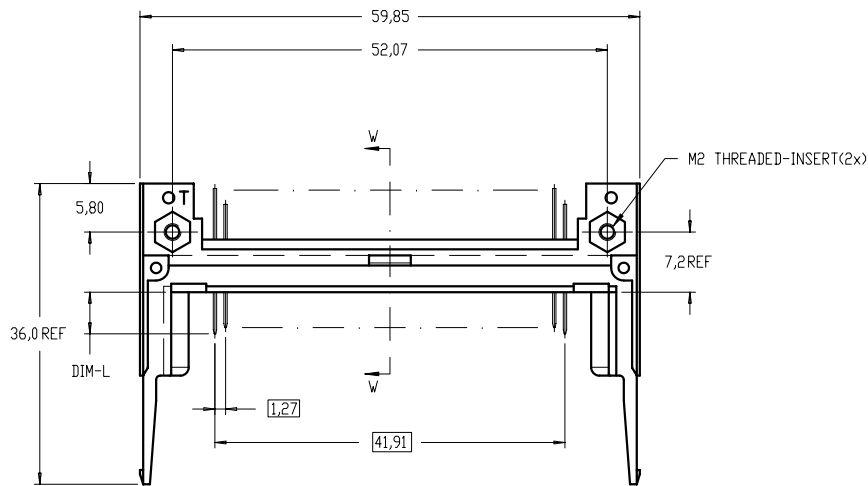
PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

mat'l. code		tolerances unless otherwise specified		CUSTOMER COPY	 www.fciconnect.com	
ltr	ecn no	dr	date	linear		projection
E				angles		title
				dr	MM	product family
				enrg		size
				chr		code
				appd		sheet
					N.T.S.	11 of
sheet index	revision sheet					

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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-003/003LF	TOP-MOUNT	BLACK	THRU-HOLE, R/A	NO	NO	YES



DETAIL-X (SCALE 15 : 1)

NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. NI
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

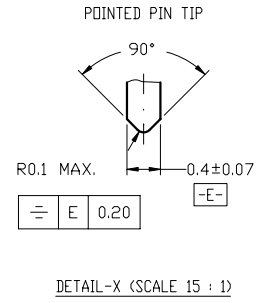
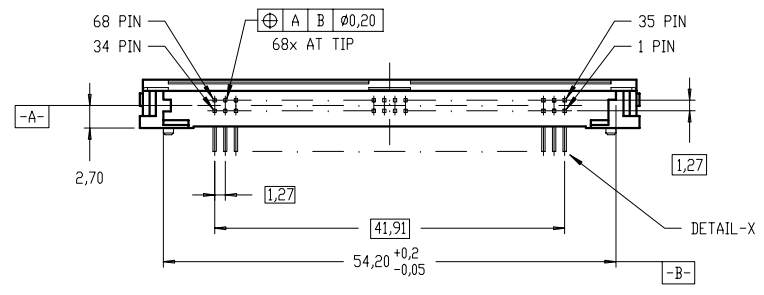
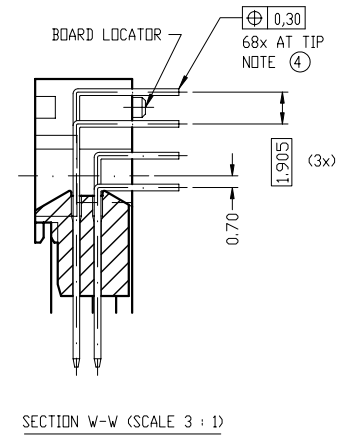
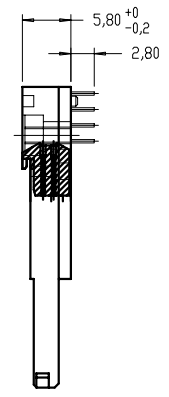
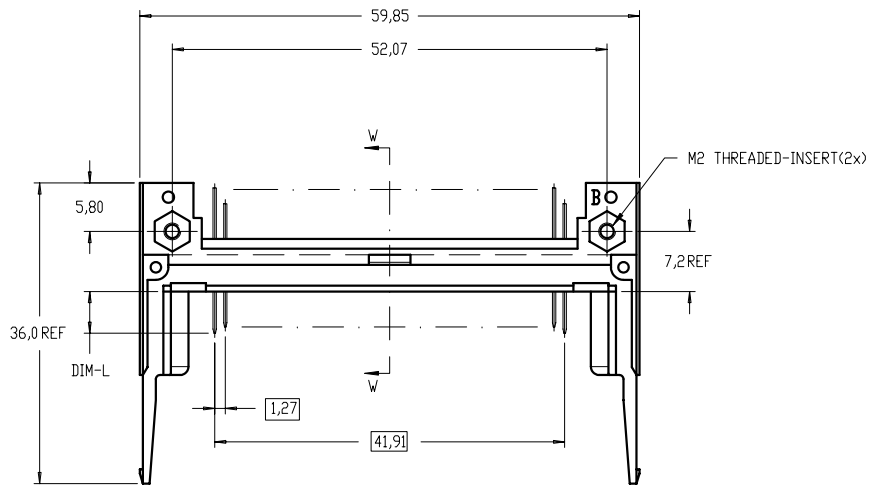
mat'l. code		tolerances unless otherwise specified		CUSTOMER COPY		 www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3	projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY
E				angles	0° ±2'	 MM	
		dr	WB JIANG 11/26/04	engr	WB JIANG 11/26/04	scale	product family PCMCIA code NT
		chr	WB JIANG 11/26/04	appd	JASON ZHOU 11/26/04	N.T.S.	size dwg no 92856 sheet 12 of
sheet index	revision sheet						cage code

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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-503/503LF	BOTTOM-MOUNT	NATURAL	THRU-HOLE, R/A	NO	NO	YES



NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. NI
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-NI
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY	FCI www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3	projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY	
E				angles	0° ±2'		product family	PCMCIA
				dr	WB JIANG 11/26/04	← MM →	code	NT
				enrg	WB JIANG 11/26/04	scale	size	dwg no
				chr	WB JIANG 11/26/04	N.T.S.	A	92856
				appd	JASON ZHOU 11/26/04		sheet	13 of
sheet index	revision sheet							

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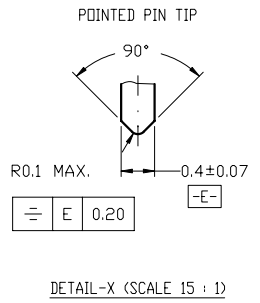
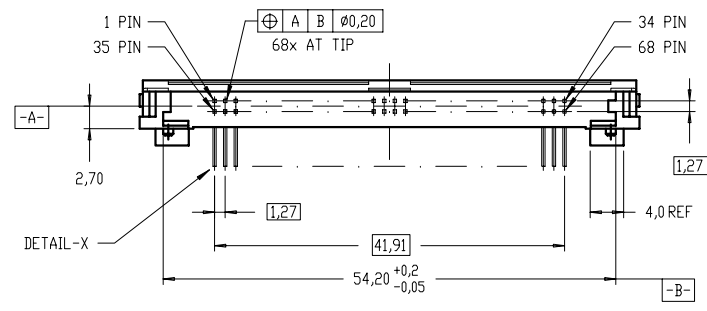
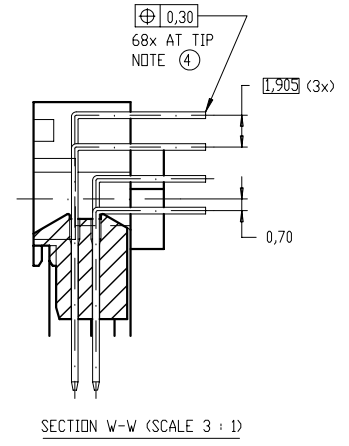
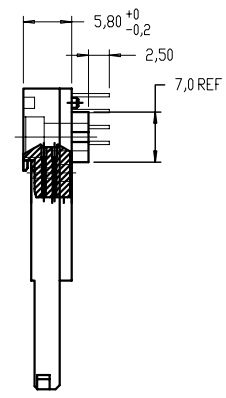
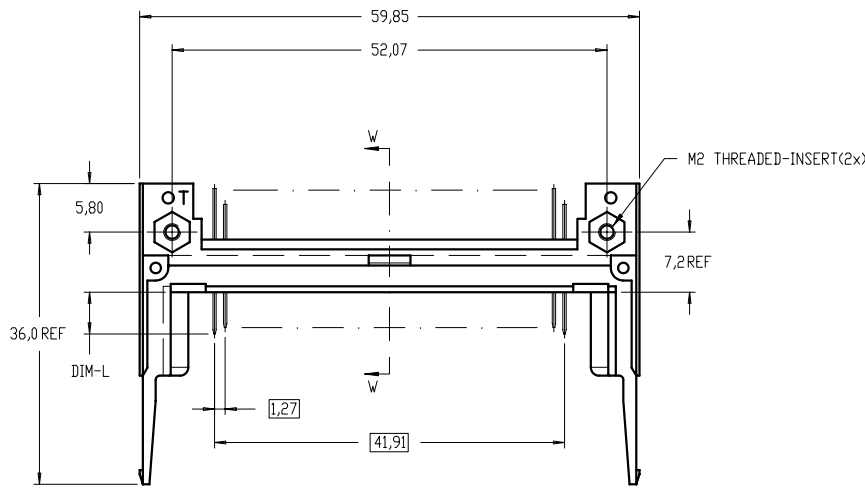
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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-080/080LF	TOP-MOUNT	BLACK	THRU-HOLE, R/A	2 mm	NO	NO



NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

A

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		 www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3		projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY	
E					angles	0° ±2'		product family PCMCIA code NT	
				dr	WB JIANG	11/26/04	 N.T.S.	size dwg no	
				enrg	WB JIANG	11/26/04		92856	
				chr	WB JIANG	11/26/04		sheet 14 of	
				appd	JASON ZHOU	11/26/04			
sheet index	revision sheet								

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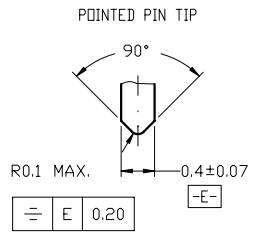
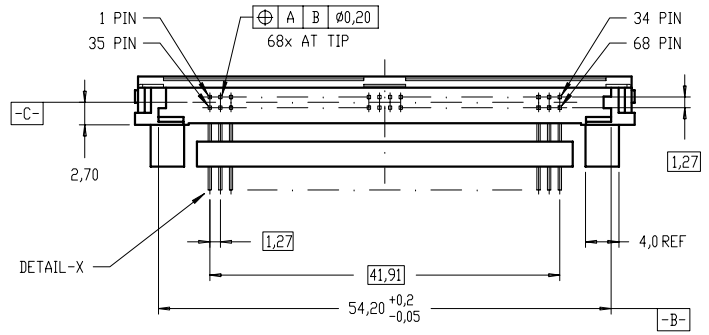
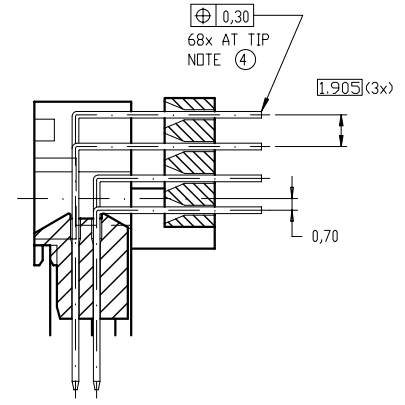
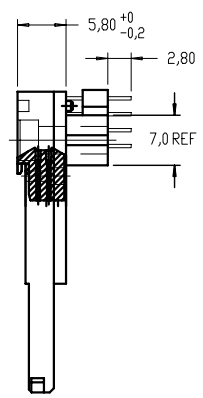
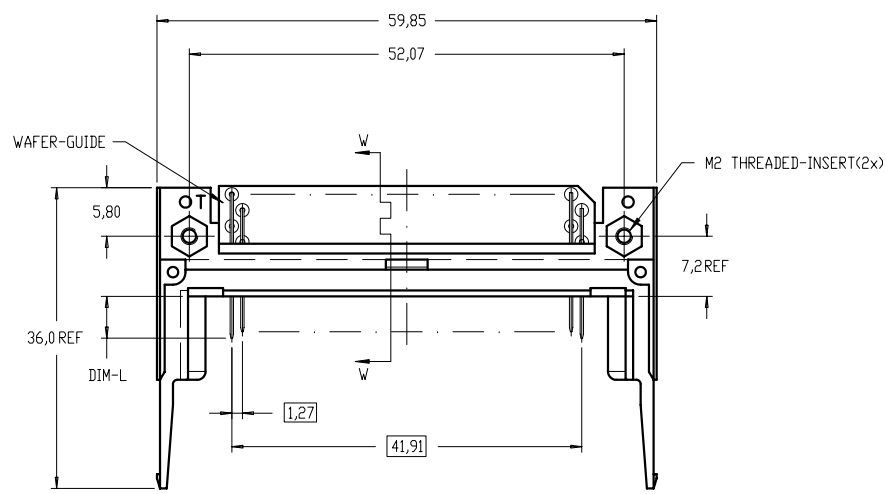
PDM: Rev:E

STATUS: Released 25/26 Printed: Sep 28, 2007

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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-020/020LF	TOP-MOUNT	BLACK	THRU-HOLE, R/A	5 mm	NO	NO



- NOTES :
- SEE TA-946 FOR PCB LAYOUT.
 - MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
 - FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
 - TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
 - IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
 - LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008
 - IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

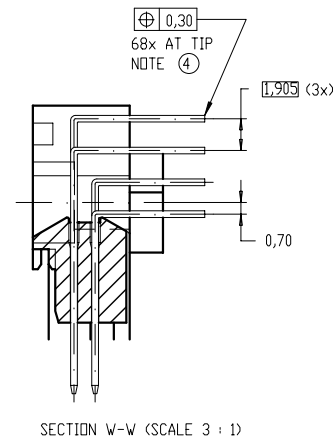
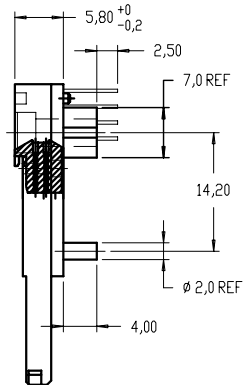
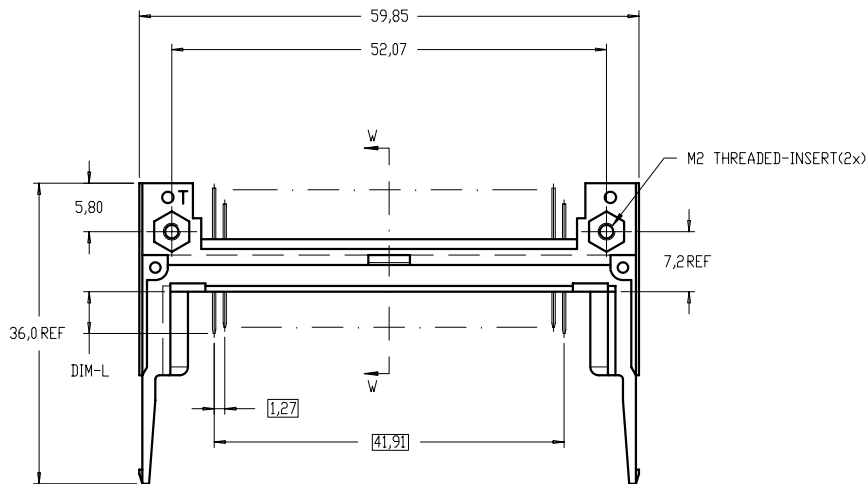
PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		 www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3	projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY		
E				angles	0° ±2°	 MM	product family	PCMCIA	code
				dr	WB JIANG 11/26/04	scale	size	dwg no	NT
				enrg	WB JIANG 11/26/04	N.T.S.	A	92856	sheet
				chr	WB JIANG 11/26/04				15 of
				appd	JASON ZHOU 11/26/04				
sheet index	revision sheet								

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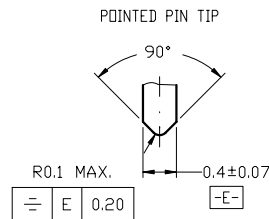
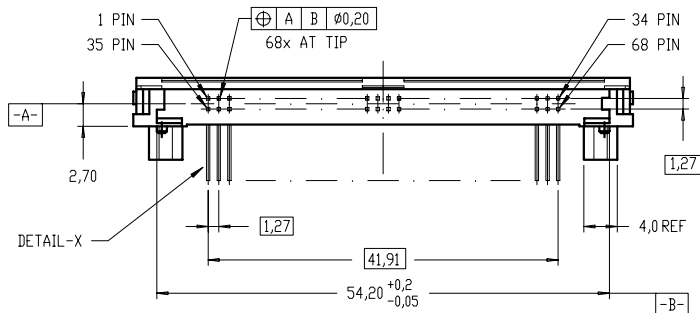
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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-010/010LF	TOP-MOUNT	BLACK	THRU-HOLE, R/A	4 mm	4 mm	NO



NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION



DETAIL-X (SCALE 15 : 1)

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		 www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3		projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY	
E					angles	0° ±2'		product family PCMCIA code NT	
				dr	WB JIANG	11/26/04	 MM	size dwg no	
				enr	WB JIANG	11/26/04		A 92856	
				chr	WB JIANG	11/26/04		sheet 16 of	
				appd	JASON ZHOU	11/26/04		scale N.T.S.	
sheet index	revision sheet							cage code	

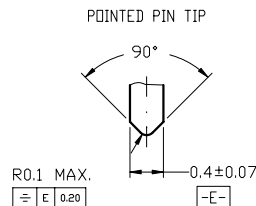
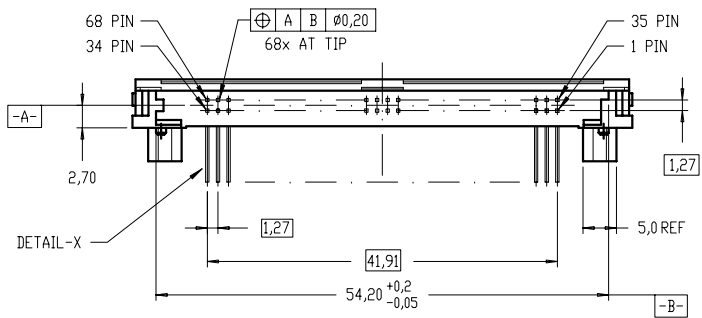
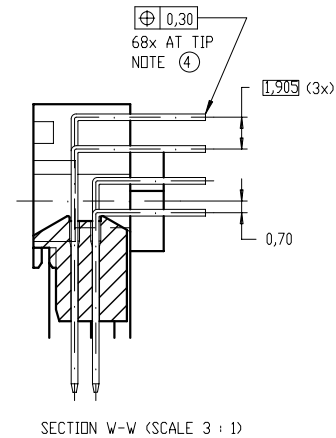
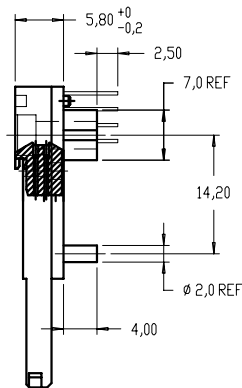
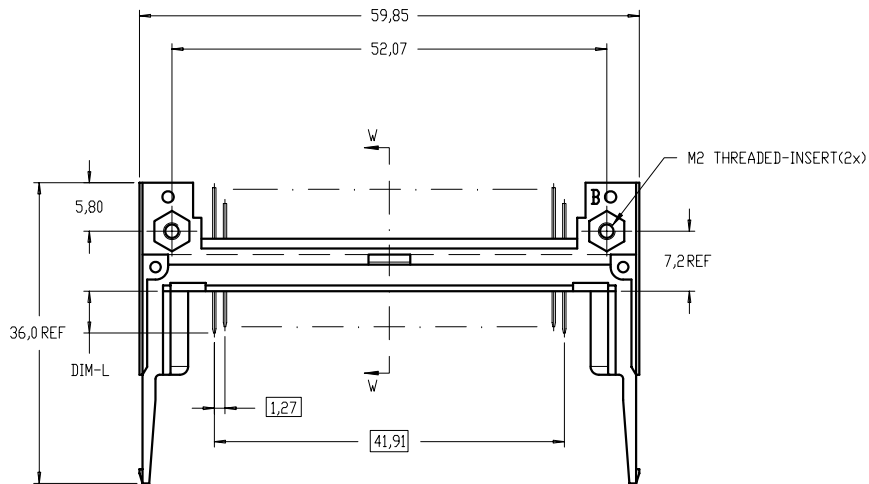
PDM: Rev:E

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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-OFF	MOUNTING SUPPORT	BOARD LOCATOR
92856-510/510LF	BOTTOM-MOUNT	NATURAL	THRU-HOLE, R/A	4 mm	4 mm	NO



NOTES :

- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL :
PLASTIC : LCP UL94V-0
PIN : PHOSPHOR BRONZE
- FINISH (PIN) :
UNDER-PLATING : 0.5 um MIN. Ni
CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni
SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb
SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN
- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

PIN NO.	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS
DIM-L	3,50 ± 0,10	5,00 ± 0,10	4,25 ± 0,10

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		 www.fciconnect.com	
ltr	ecn no	dr	date	linear	±.3		projection	title 68P EJECTOR HEADER ASSEMBLY FOR PCMCIA/JEIDA TYPE-I/II/III HEADER ASSEMBLY	
E					angles	0° ±2'		product family PCMCIA code NT	
				dr	WB JIANG	11/26/04	 MM	size dwg no	
				enr	WB JIANG	11/26/04		A 92856	
				chr	WB JIANG	11/26/04		sheet 17 of	
				appd	JASON ZHOU	11/26/04		scale N.T.S.	
sheet index	revision sheet								